# U.S. UTILITY Patent Application

| | O.I.P.E. | PATENT DATE |
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| M.H | ③ | |
| SCANNED HKM Q.A. | | |

PATENT NUMBER

| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
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| 09/745919 | | 370 | 220 | 2661 | K. Dang |
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**APPLICANTS**
Thomas Bayerl
Christopher Tumas

**TITLE**
Use of bus hold to prevent bus contention between high speed processor and slow peripheral

PTO-2040
12/89

## ISSUING CLASSIFICATION

| ORIGINAL | | CROSS REFERENCE(S) | |
|---|---|---|---|
| CLASS | SUBCLASS | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) |

INTERNATIONAL CLASSIFICATION

☐ Continued on Issue Slip Inside File Jacket

| TERMINAL DISCLAIMER ☐ | DRAWINGS | | | CLAIMS ALLOWED | |
|---|---|---|---|---|---|
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

(Assistant Examiner) _____ (Date)

**NOTICE OF ALLOWANCE MAILED**

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

**ISSUE FEE**

| Amount Due | Date Paid |
|---|---|

(Primary Examiner) _____ (Date)

☐ The terminal _____ months of this patent have been disclaimed.

**ISSUE BATCH NUMBER**

(Legal Instruments Examiner) _____ (Date)

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